

第20回アジア南太平洋設計自動化会議 (ASP-DAC 2015)

日程: 2015年1月19日(月)～22日(木)

場所: [幕張メッセ](#)、千葉市

English / 日本語

[ASP-DAC Bylaws](#)

ホーム
アワード
フォト・ギャラリー
ようこそASP-DAC2015へ
テクニカル・プログラム
ハイライト
基調講演
特別セッション
デザイナーズ・フォーラム
学生リサーチ・フォーラム
チュートリアル
デザイン・コンテスト
オーサーズ・ガイド
参加案内
参加登録
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パンケット
コミッティーの紹介
ASP-DACの履歴
姉妹会議
主催・後援
企業展示
連絡先

20th Anniversary Awards



ASP-DAC Foundation Award

Dr. Tokinori Kozawa
 Dr. Kenji Yoshida
 Prof. Tatsuo Ohtsuki

ASP-DAC Leadership Award

Prof. Tatsuo Ohtsuki (1st GC)
 Prof. Isao Shirakawa (2nd GC)
 Dr. Tokinori Kozawa (3rd GC)
 Prof. Richard M. M. Chen (4th GC)
 Prof. Qian-Ling Zhang (4th GC)
 Dr. Kenji Yoshida (5th GC)
 Prof. Satoshi Goto (6th GC)
 Dr. Sunil D. Sherlekar (7th GC)
 Prof. Hiroto Yasuura (8th GC)
 Prof. Masaharu Imai (9th GC)
 Prof. Ting-Ao Tang (10th GC)
 Dr. Fumiyasu Hirose (11th GC)
 Prof. Hidetoshi Onodera (12th GC)
 Prof. Chong-Min Kyung (13th GC)
 Dr. Kazutoshi Wakabayashi (14th GC)
 Prof. Youn-Long Lin (15th GC)
 Prof. Kunihiro Asada (16th GC)
 Prof. Sri Parameswaran (17th GC)
 Prof. Shinji Kimura (18th GC)
 Prof. Yong Lian (19th GC)
 Prof. Yajun Ha (19th GC)
 Dr. Kunio Uchiyama (20th GC)

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ASP-DAC Most Frequent Author Award

Prof. Xianlong Hong

ASP-DAC Frequent Author Award

Prof. Massoud Pedram
 Prof. Martin D. F. Wong

ASP-DAC Most Frequently Cited Paper Award

Energy-Aware Mapping for Tile-based NoC Architectures Under Performance Constraints

Jingcao Hu and Radu Marculescu, 2003



Supporter's Exhibition:



ASP-DAC Frequently Cited Paper Award

Battery-Driven System Design: A New Frontier in Low Power Design

Kanishka Lahiri, Sujit Dey, Debashis Panigrahi, and Anand Raghunathan, 2002

LEneS: Task Scheduling for Low-Energy Systems Using Variable Supply Voltage Processors

Flavius Gruian and Krzysztof Kuchcinski, 2001

ASP-DAC Most Frequently Cited Author Award

Prof. Jason Cong

ASP-DAC Frequently Cited Author Award

Prof. Andrew B. Kahng

Prof. David Z. Pan

Distinguished Service Award for ASP-DAC

(presented by ACM SIGDA, IEEE CEDA, and Sister Conferences (DAC/DATE/ICCAD))

Prof. Tatsuo Ohtsuki

Prof. Hiroto Yasuura

Prof. Hidetoshi Onodera

Best Paper Award

1C-1: Quantitative Modeling of Racetrack Memory, A Tradeoff among Area, Performance, and Power

Chao Zhang, Guangyu Sun, Weiqi Zhang (CECA, Peking University, China), Fan Mi, Hai Li (University of Pittsburgh, U.S.A.), Weisheng Zhao (Spintronics Interdisciplinary Center, Beihang University, China)

University LSI Design Contest Awards

Best Design Award

1S-1: An HDL-Synthesized Gated-Edge-Injection PLL with A Current Output DAC

Dongsheng Yang, Wei Deng, Tomohiro Ueno, Teerachot Siriburanon, Satoshi Kondo, Kenichi Okada, Akira Matsuzawa (Tokyo Institute of Technology, Japan)

Special Feature Award

1S-23: Circuit and Package Design for 44GB/s Inductive-Coupling DRAM/SoC Interface

Akira Okada, Abdul Raziz Junaidi, Yasuhiro Take, Atsutake Kosuge, Tadahiro Kuroda (Keio University, Japan)

10-Year Retrospective Most Influential Paper Award

Thermal-driven multilevel routing for 3-D ICs

Jason Cong, Yan Zhang

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